

PTO-1449		Application No. 09/910,591		Applicant(s) Daniel E. Lenoski, et al.	
Information Disclosure Citation In an Application		Docket Number 062986.0211	Group Art Unit 2188	Filing Date 07/20/01	

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
		6,085,263	07/04/00	Sharma, et al.			
R68	B	US2002/0087807A1	07/04/02	Gharachorloo, et al.			
R68	C	US2003/0115422A1	06/19/03	Spencer, et al.			
R68	D	US2003/0120875 A1	06/26/03	Bourne, et al.			
R68	E	US2004/0088487 A1	05/06/04	Barroso, et al.			
	F						
	G						
	H						
	I						
	J						
	K						
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FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	N							
	O							

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
R68	P	Culler, et al. "Parallel Computer Architecture", San Francisco Morgan-Kaufman, cover pages and pp. 553-559, 1998	1998
		DATE CONSIDERED	

EXAMINER *Reginald H. Bragdon*

11-21-2005

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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